

FIGURE 1

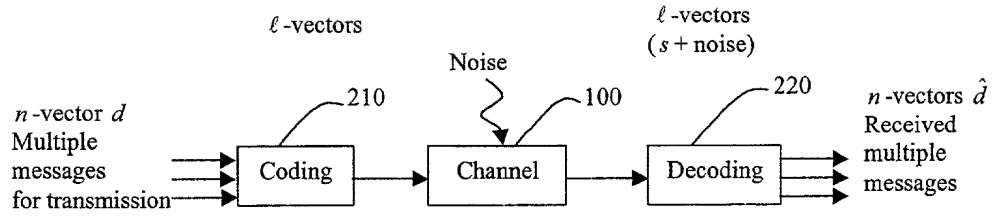


FIGURE 2

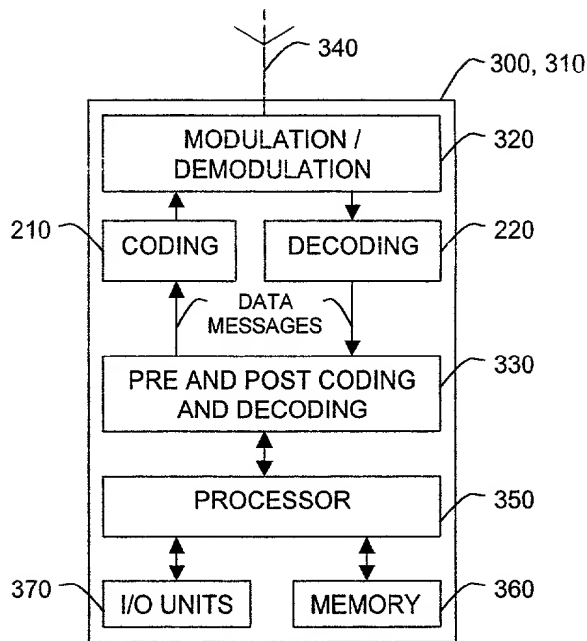


FIGURE 3A

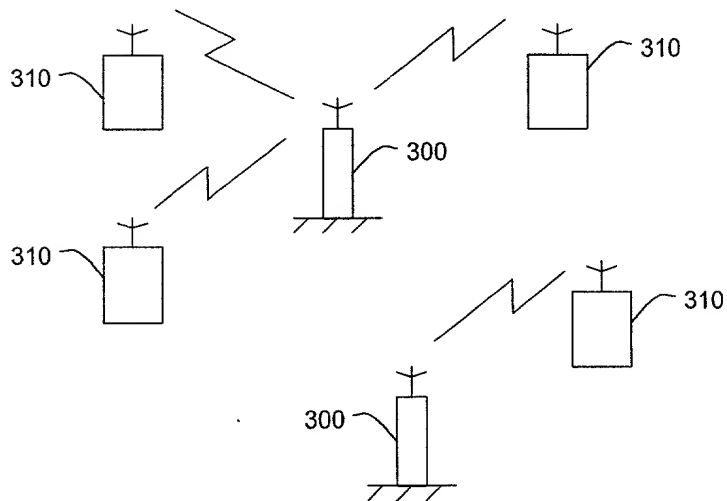


FIGURE 3B

Input

Output

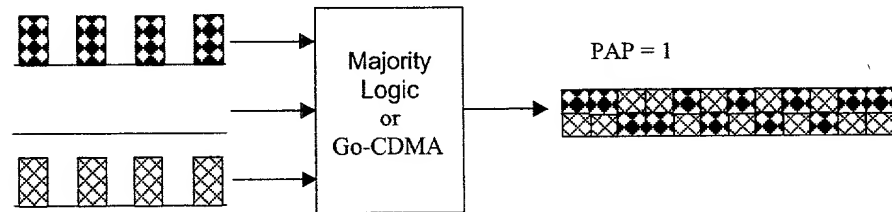
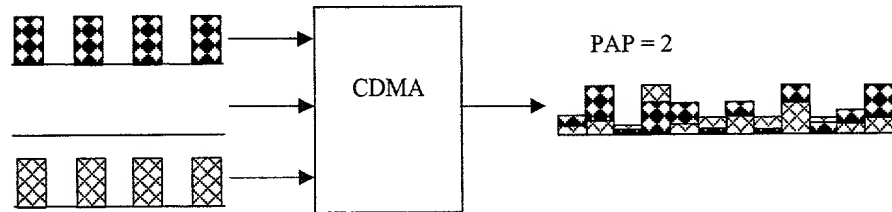
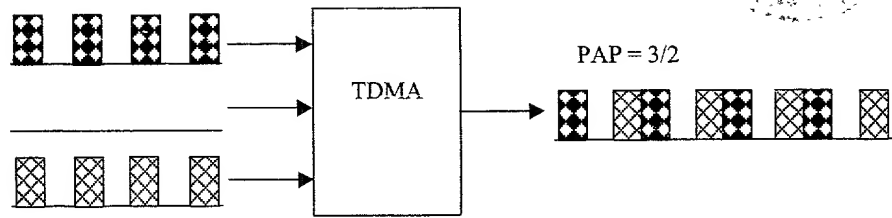


FIGURE 4

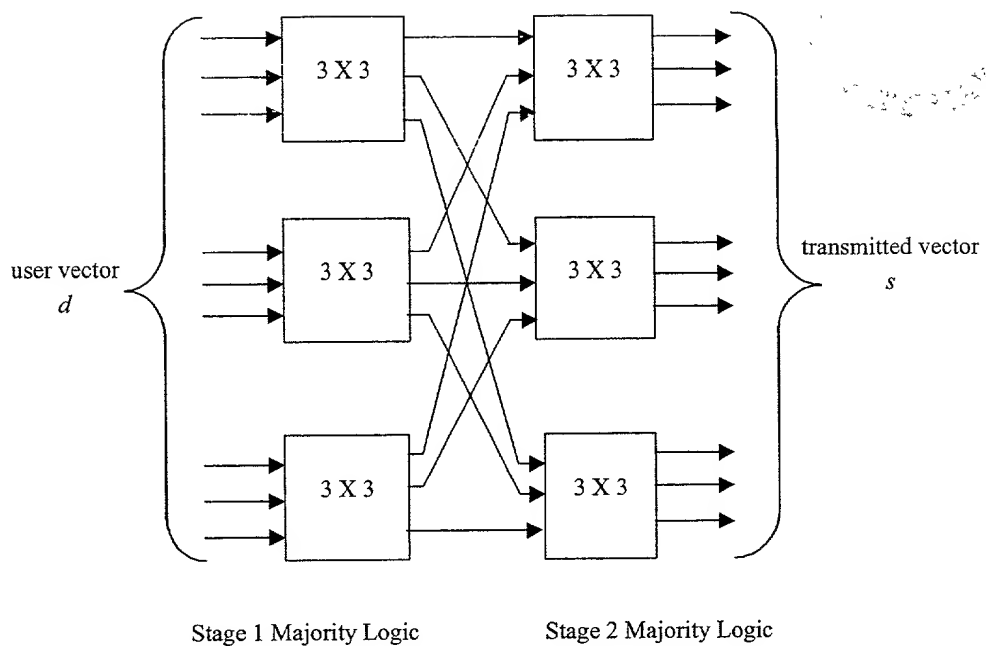


FIGURE 5

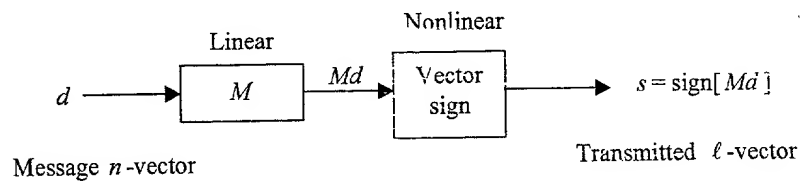


FIGURE 6

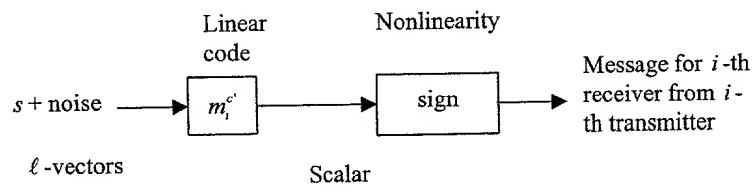


FIGURE 7

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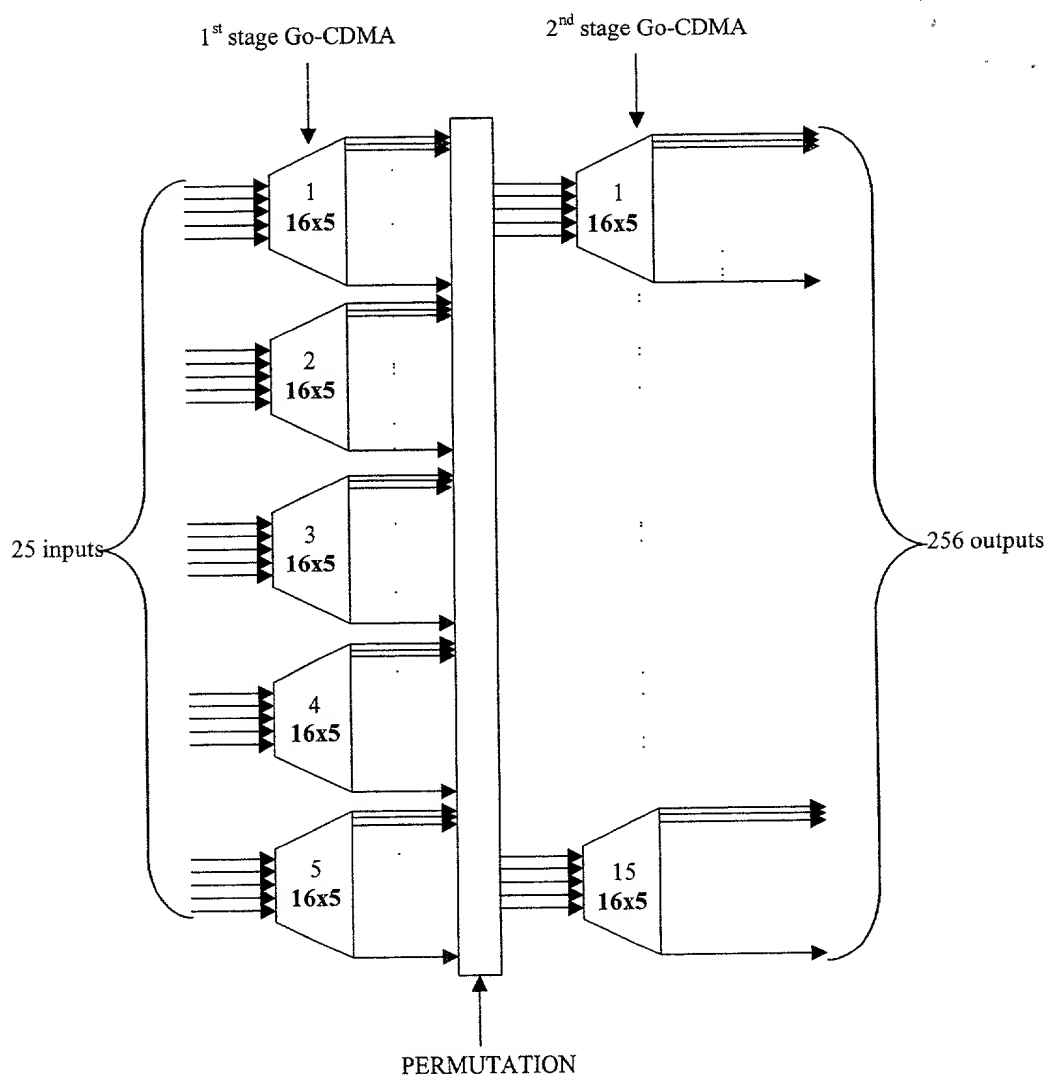


FIGURE 8

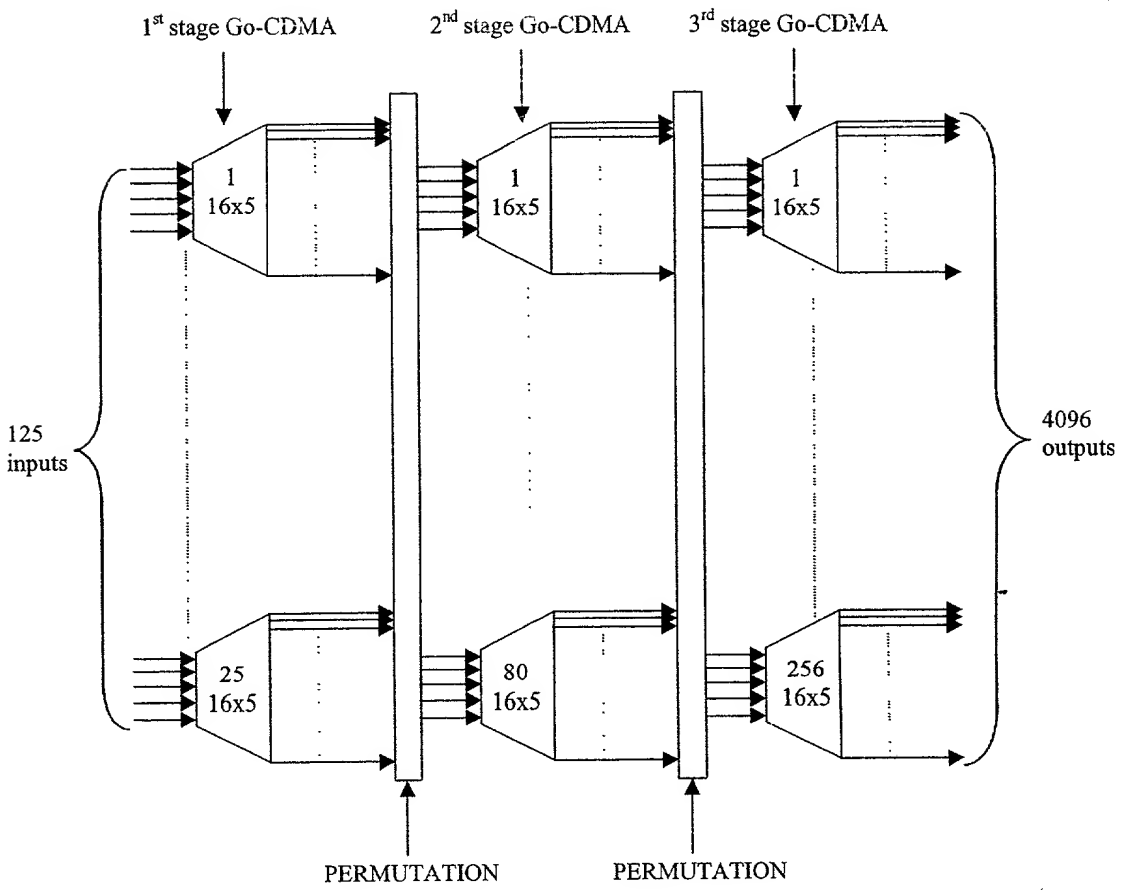


FIGURE 9

FIGURE 10

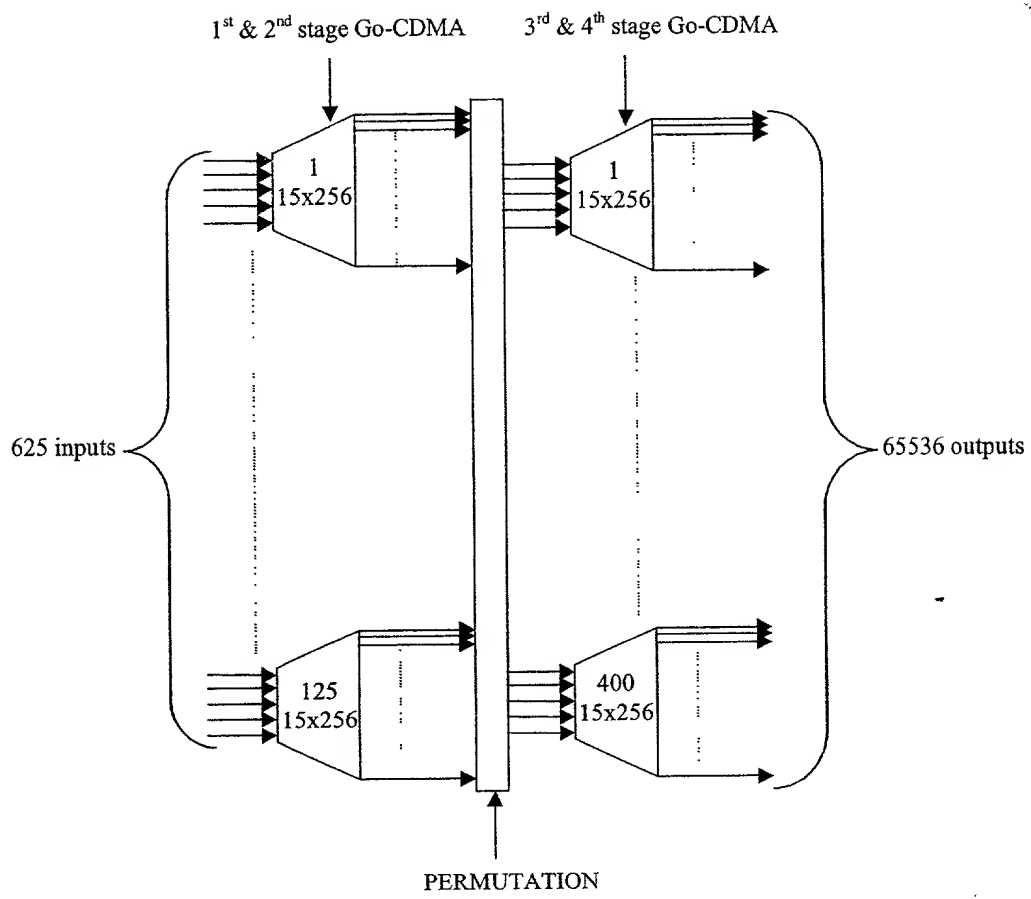


FIGURE 10

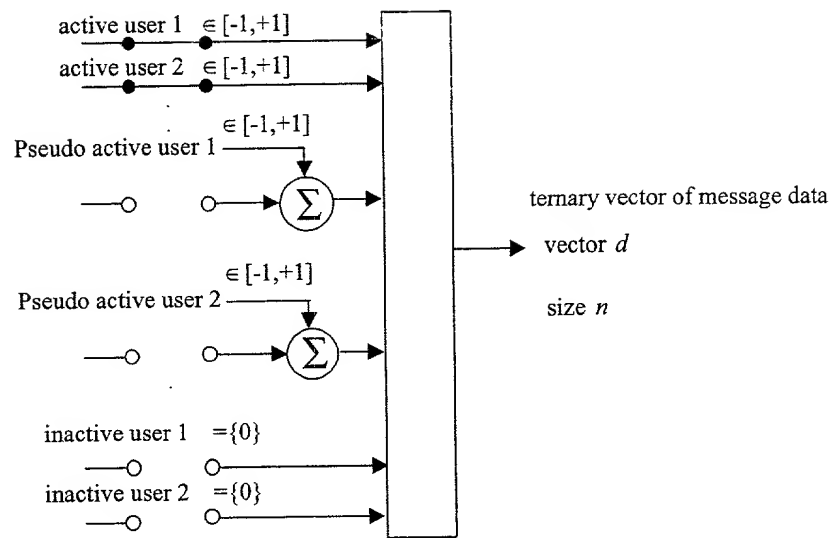


FIGURE 11

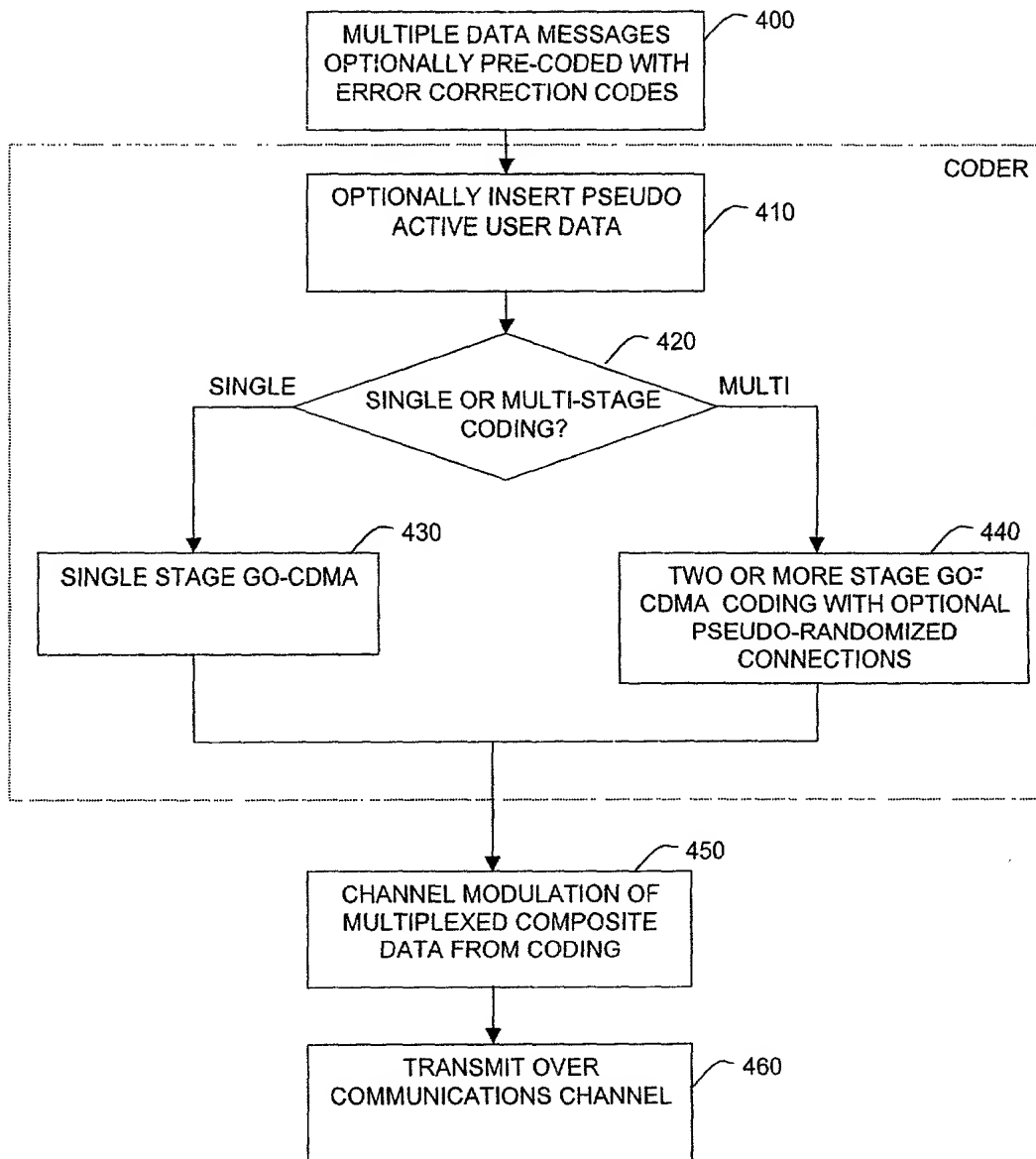


FIGURE 12

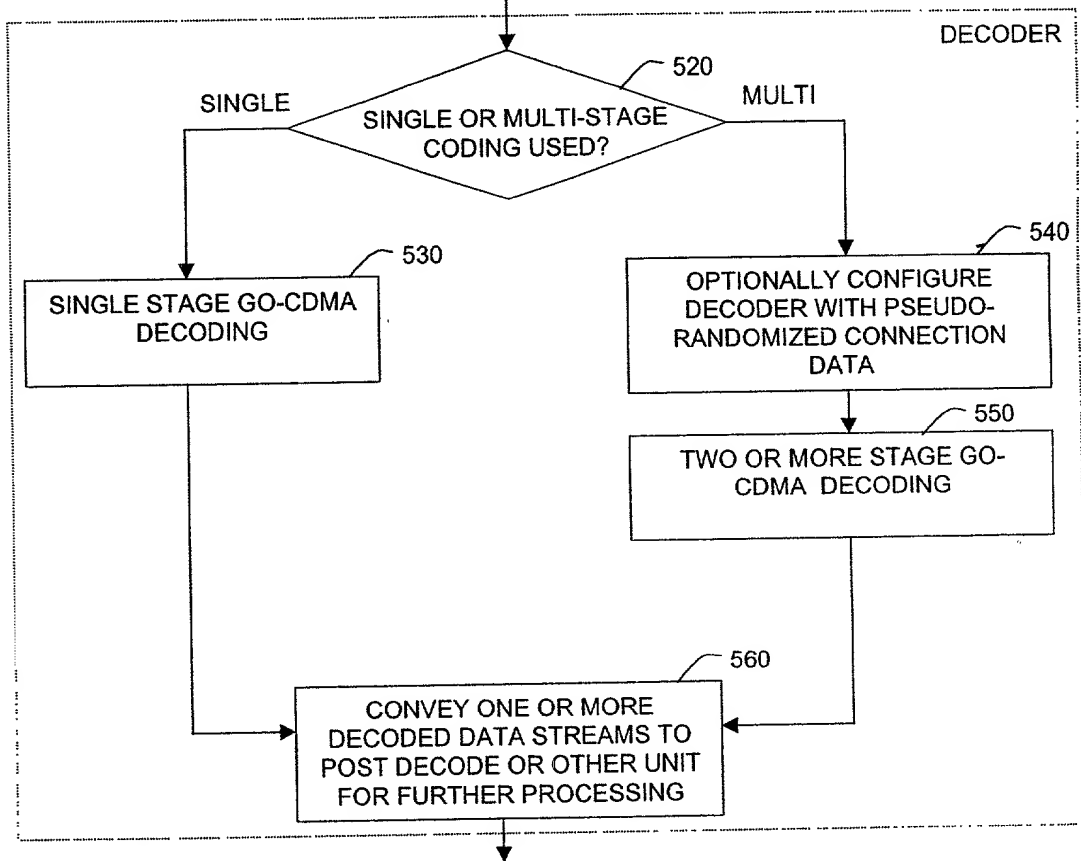
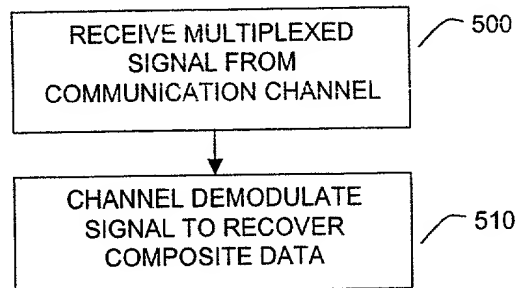


FIGURE 13

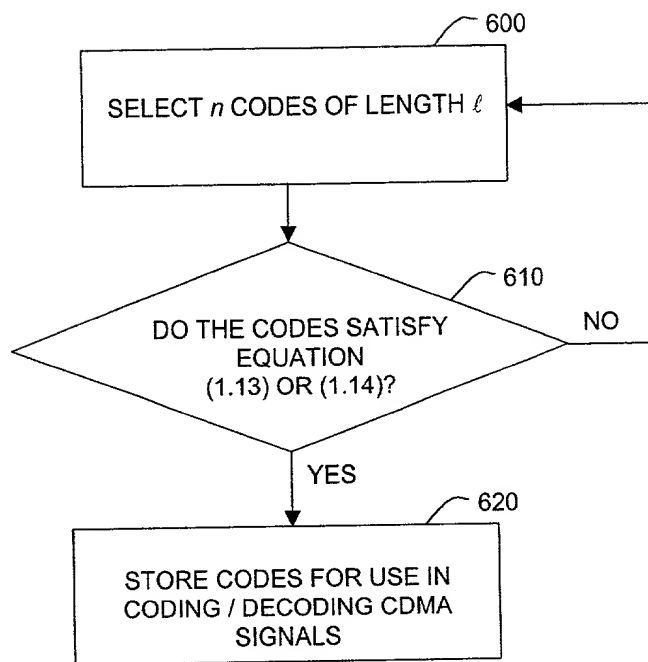


FIGURE 14

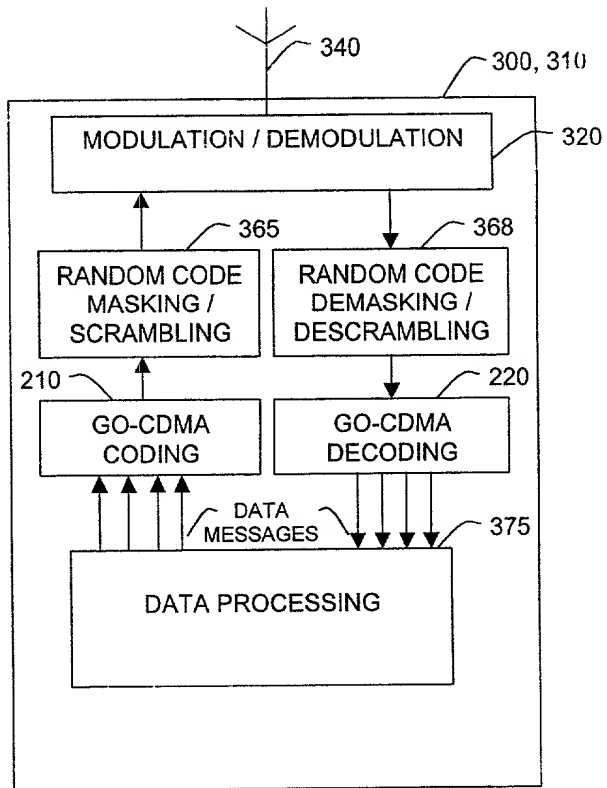


FIGURE 15A

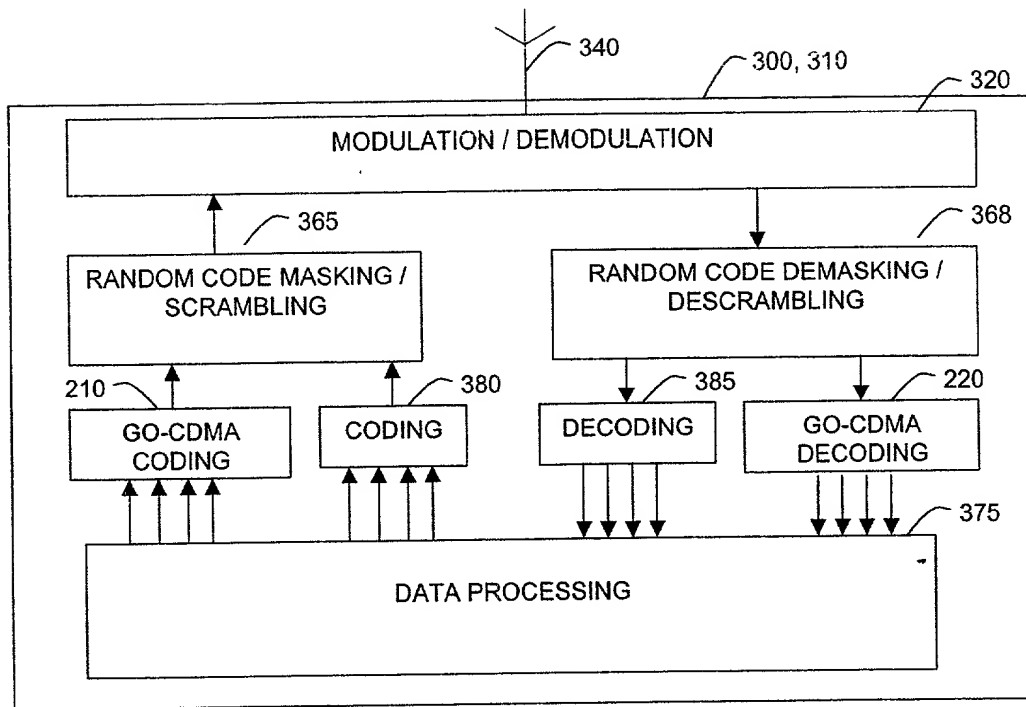


FIGURE 15B